

Preliminary Amendment  
Application No. 09/728,889  
November 21, 2002

C' -- The blocks and functions implemented in a microprocessor ( $\mu P$ ) or a DSP are shown within the dashed line of Fig. 8. Each of these blocks is further described below.

1. Block  $K_t^1$  multiplies the commanded torque  $T_{crank}^*$  by a well known formula to obtain the motor torque producing current  $I_{qs}^*$ .

2. PID block represents a proportional-integral-derivative voltage regulator, having as its input a voltage error (Difference between the commanded voltage,  $V_{reg}^*$  and the actual, measured DC voltage,  $V_{dc}$ ). The most general form the voltage regulator is shown.

3.  $K_v$  block scales the output of the voltage PID controller and converts it to the commanded generator torque producing current  $i_{qs}^*$ .

4. Switches S1 and S2 are the software switches, which select the operating mode:

S1 switch selects between the motor (upper position) and the generator (lower position) commanded torque producing current  $i_{qs}^*$ . The switch S1 also selects the number of machine poles (high number for motoring operation, upper position and lower number for generator operation, lower position).

S2 selects between low speed flux command (upper position) and high speed generator or motor flux (lower position).

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C1  
cont.  
5. Flux estimator block calculates the machine actual flux from the commanded machine voltage  $V_{\alpha\beta}^*$  and flux position ( $\sin\theta$ ,  $\cos\theta$ ) and the measured machine currents  $i$  and the DC voltage  $V_b$ .

6. Flux Optimizer block performs two functions:

(a) It adjusts the commanded flux for high speed operation in function of the machine synchronous speed  $\omega_o$

(b) It regulates the machine flux level in high speed mode by comparing the adjusted commanded flux with the estimated flux and processing their difference through a PI flux regulator. The output from the regulator defines the commanded flux producing current,  $I_{ds}^*$ .

7. Slip calculation block computes the exact commanded slip speed  $\omega_{slip}^*$  required by vector control:

$$\omega_{slip}^* = \frac{L_m}{T_r} \frac{i_{qs}^*}{i_{ds}^*}$$

where  $T_r$  is the rotor time constant,  $L_r/R_r$

8. The slip speed is integrated in this block to obtain the "slip angle"  $\theta_{slip}$

9. This block adds the commanded slip angle  $\theta_{slip}$  to the measured rotor position angle  $\theta_r$  to obtain the angle  $\theta$  which represents the position of the rotor flux.

10. The derivative block calculates the machine synchronous speed  $\omega_o$  by differentiating rotor flux position,  $\theta$ .

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C1  
cont.  
11. The sine and cosine generator calculates these functions of the angle, for every position of the rotor flux.

12. This block transforms the measured currents from stationary ( $\alpha$ - $\beta$ ) to synchronously rotating (d-q) reference frame using  $\sin\theta$  and  $\cos\theta$  functions.

13. This block represents 2-channel current regulator, in the synchronous reference frame. It compares commanded  $i_q^*$  and  $i_d^*$  currents with the measured  $i_d$  and  $i_q$  currents and processes their respective differences through a PI regulator. The output of the current regulator represents the commanded voltages  $V_{dq}$ .

14. This block transforms the commanded voltages  $V_{dq}^*$  from synchronous (d-q) to stationary ( $\alpha$ - $\beta$ ) reference frame.

15. This block transforms commanded voltages from 2-phase stationary to 9-phase stationary reference frame.

16. This block transforms measured currents from 9-phase stationary to 2-phase stationary reference frame.

17. This block ( $K^{-1}$ ) scales the measured DC voltage to signal level, for control purposes.

The blocks that are outside of the microprocessor or the DSP are described as follows:

A. The power inverter, which converts the battery DC into AC power during motoring and the generator AC into DC power during

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*C1  
cancel* generation. In this particular example, the inverter is shown with nine phases at the output.

B. FPGA (Field Programmable Gate Array) which is used to define the PWM pattern for control of the inverter switches. (The PWM pattern determines the "on" and "off" time for each of the inverter switches)

C. The Induction Machine, which is designed so that it can be controlled with a Pole-Phase Modulation method. In this particular example, the induction machine is shown with nine phases.

D. The position sensor, which measures the instantaneous rotor position  $\theta_r$ . In this example, the position sensor is shown to be an incremental encoder. --

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IN THE CLAIMS

Please substitute claim 1 for the pending claim with the same number respectively:

*C2* -- 1. (Twice Amended) A system comprising:  
an induction machine with a stator and a rotor, said stator having a plurality of phase windings;